

Notice of References Cited

Application/Control No.

09/733,243

Applicant(s)/Patent Under
Reexamination
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Examiner

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Art Unit

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5688700 /	11-1997	Kao et al	438/289
	B	US-5597752 /	01-1997	Niwa	438/305
	C	US-4758528 /	07-1988	Goth et al	438/301
	D	US-			
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al, "Silicon Processing for the VLSI", Volume 1, Pages 191-194, 1986. ✓
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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